

SHOW ME THE WAY

$\boxed{\text{GTR}}_3 + \left| \begin{array}{c} \textcircled{1} \\ \gamma \beta \gamma \beta \end{array} \right| \parallel \left| \begin{array}{c} \times \\ \gamma \end{array} \right| \left| \begin{array}{c} \sim \\ \beta \end{array} \right| \left| \begin{array}{c} \perp \\ \gamma \end{array} \right| \left| \begin{array}{c} \% \\ \beta \end{array} \right| \left| \begin{array}{c} \% \\ \gamma \end{array} \right| \left| \begin{array}{c} \gamma \beta \gamma \beta \end{array} \right| \parallel$

INT 8 RIDE CH-Y ① FIG 4's OH 7 6 5 4 3 2 1

A 16 sim. NO CYM? ① 8th, 16th

B 4 sim TWO HAND HH; OUT: U U

Diagram showing four boxes with their sizes and instructions:

- Box C: Size 8. Instructions: WASH; FILL; PUSH'S 4+ 3RD, 7th
- Box INT: Size 4. Instructions: CH-Y; ① LAST
- Box A: Size 16. Instructions: A.A.
- Box B: Size 4. Instructions: A.A.

Diagram illustrating the components of a 128-bit block cipher:

- C**: 12 bits, A.A. (All-At-Once)
- Solo**: 8 bits, CH-Y, SIM, ① LAST
- A**: 8 bits, A.A. (All-At-Once)
- B**: 4 bits, A.A. (All-At-Once)

Diagram illustrating memory layout:

- Block 1: Labeled 'C' (Character Array), size 16, associated with 'A.A.' (Address Array).
- Block 2: Labeled 'INT' (Integer Array), size 4, associated with 'CH-Y' (Character Array), and marked as '① LAST'.

END
1 OF 5th
LONG

(BOOM BAP GRÜß?)

↑ ↓ ↑ ↓ NO CYM,
OR JUST END