

NEUTRON DANCE (↑)

DRUM FILL ?

WASH

4

4

INT

4

HH,
OH PUSHES
SHORT

Diagram illustrating memory layout and simulation:

- Left Side:** A box labeled **V** with address **8** below it. To its right is a box labeled **INT** with address **4 + 2** below it. Above the **INT** box is the text "w/ PUSH WASH" with an upward arrow. To the left of the **INT** box is the text "sim." with a small vertical line.
- Right Side:** Three boxes are shown. The first box is labeled **V** with address **8** below it. The second box is labeled **INT** with address **4** below it. The third box is labeled **C** with address **8** below it. Above the **INT** box is the text "sim" with a bracket. Above the **C** box is the text "RIDE 4 WASH 4" with a bracket.

Diagram illustrating a 32-bit register file structure with four registers:

- INT (2 bits):** Connected to V (8 bits) via a line labeled "BR3, PUSH OUT".
- V (8 bits):** Connected to C (8 bits) via a line labeled "CH 4" and "HH 4".
- C (8 bits):** Connected to BR (4 + 4 vox) via a line labeled "sim" and "CLHH, PUSHES".
- BR (4 + 4 vox):** No connections shown.

A hand-drawn diagram of a 3-4-8 drum kit layout. It consists of three square boxes representing drums, arranged horizontally. The first box on the left is labeled 'C' (Cymbal) and has the number '8' below it. To its right is the text 'RIDE 4' and 'HH 4' with a curved arrow pointing to the right. The middle box is labeled 'BR' (Bass Drum) and has the number '4' below it. To its right is the text 'RIM/BB' and 'BD w/ BASS' with a curved arrow pointing to the right. Below this text is the red text 'BR 3 OF 4th'. The third box on the right is labeled 'C' (Cymbal) and has the number '8' below it. To its right is the text 'RIDE' and 'BIG FILLZ'.

INT^{WASH} + | [] i i ° ||